Troubleshooting list:

The following troubleshooting list should help you to clarify the most detected errors and give you a helping hand how to avoid such faulty behaviors. Also the current versions of the existing FW-stacks are included. These versions should be double checked by the user in advance.

Issue	Problem	Solution
SPC3	During operation with S7 the slave reacts	The SAP-List Pointer at address 0x17
3r 03	in a strange way (normally it doesn't	doesn't point to a RAM cell inside of the
	react anymore)	SPC3 where 0xff is stored.
SPC3	Sporadically read or write cycles to the	Maybe this collides with the specified
01 00	SPC3 doesn't work properly.	timing 37 or 79. It's also important to
	or de decent ment propony.	check the signal form. Even small spikes
		can lead to sporadic problems.
SPC3	Although the parameterization telegram	After having acknowledged these both
	and the configuration telegram are	telegrams the user has to take care that
	acknowledged the SPC3 doesn't branch	the first input-data have to be stored in
	to Data Exchange.	the SPC3 (Input-Update)
SPC3	During start up a GC-Unclear is not	GC will only be indicated when the
0.00	indicated to the user.	received GC-Command distinguishes
		from the value stored in
		R_GC_Command. If the user always
		wants to get an indication then the cell
		R_GC_Command must always be
		overwritten with 0xff.
SPC3	The slave doesn't branch to data	Maybe the GSD-file says Fail_Safe =1
	exchange or the slaves branches to a	and in the Mode Register the Bit
	new start up during data exchange while	SPEC_CLEAR_MODE is set to 0.
	the master is in clear mode.	
DPC31	ASIC DPC31 Step B is locked for	
	synchronous mode (PA)	
DPS2		Current FW V1.30
DPSE	#define DPSE_DP is not seperately	Error in V1.21
5505	possible	E : 1/4.04
DPSE	The service Set_slave_addr is wrong	Error in V1.21
51 OL	The convice cot_clave_add to mong	
		Error in V1 22
DPSE	Function DPSE_WRITE_REQ() with	Error in V1.22
		Error in V1.22
DPSE	Function DPSE_WRITE_REQ() with return value DPSE_DELAY is wrong.	
DPSE DPSE	Function DPSE_WRITE_REQ() with return value DPSE_DELAY is wrong. Intel/Motorola-format during INITIATE	Error in V1.22
DPSE DPSE DPSE	Function DPSE_WRITE_REQ() with return value DPSE_DELAY is wrong.	Error in V1.22 Error in V1.22
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DPSE DPSE DPSE	Function DPSE_WRITE_REQ() with return value DPSE_DELAY is wrong. Intel/Motorola-format during INITIATE Interrupt also enabled during polling	Error in V1.22 Error in V1.22 Current FW V1.23
DPSE DPSE DPSE	Function DPSE_WRITE_REQ() with return value DPSE_DELAY is wrong. Intel/Motorola-format during INITIATE Interrupt also enabled during polling pbc_open_device sometimes returns the	Error in V1.22 Error in V1.22 Current FW V1.23 Error in V1.0:
DPSE DPSE DPSE	Function DPSE_WRITE_REQ() with return value DPSE_DELAY is wrong. Intel/Motorola-format during INITIATE Interrupt also enabled during polling	Error in V1.22 Error in V1.22 Current FW V1.23 Error in V1.0: The structure element
DPSE DPSE DPSE	Function DPSE_WRITE_REQ() with return value DPSE_DELAY is wrong. Intel/Motorola-format during INITIATE Interrupt also enabled during polling pbc_open_device sometimes returns the	Error in V1.22 Error in V1.22 Current FW V1.23 Error in V1.0: The structure element dpc31.com_user_ram_segments is not
DPSE DPSE DPSE	Function DPSE_WRITE_REQ() with return value DPSE_DELAY is wrong. Intel/Motorola-format during INITIATE Interrupt also enabled during polling pbc_open_device sometimes returns the	Error in V1.22 Error in V1.22 Current FW V1.23 Error in V1.0: The structure element dpc31.com_user_ram_segments is not initialized in the existing example.
DPSE DPSE DPSE	Function DPSE_WRITE_REQ() with return value DPSE_DELAY is wrong. Intel/Motorola-format during INITIATE Interrupt also enabled during polling pbc_open_device sometimes returns the	Error in V1.22 Error in V1.22 Current FW V1.23 Error in V1.0: The structure element dpc31.com_user_ram_segments is not initialized in the existing example. Work around: Initialisation in sys_main.c
DPSE DPSE DPSE V1SL	Function DPSE_WRITE_REQ() with return value DPSE_DELAY is wrong. Intel/Motorola-format during INITIATE Interrupt also enabled during polling pbc_open_device sometimes returns the error PBC_INITF_LESS_MEM	Error in V1.22 Error in V1.22 Current FW V1.23 Error in V1.0: The structure element dpc31.com_user_ram_segments is not initialized in the existing example. Work around: Initialisation in sys_main.c with DPC31_USER_RAM_SEGMENTS
DPSE DPSE DPSE	Function DPSE_WRITE_REQ() with return value DPSE_DELAY is wrong. Intel/Motorola-format during INITIATE Interrupt also enabled during polling pbc_open_device sometimes returns the error PBC_INITF_LESS_MEM Slave is operating correct with S7 400. If	Error in V1.22 Error in V1.22 Current FW V1.23 Error in V1.0: The structure element dpc31.com_user_ram_segments is not initialized in the existing example. Work around: Initialisation in sys_main.c with DPC31_USER_RAM_SEGMENTS Error in V1.0:
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DPSE DPSE DPSE V1SL	Function DPSE_WRITE_REQ() with return value DPSE_DELAY is wrong. Intel/Motorola-format during INITIATE Interrupt also enabled during polling pbc_open_device sometimes returns the error PBC_INITF_LESS_MEM Slave is operating correct with S7 400. If the cyclic transmission of the bus parameters is switched on, the slave fails.	Error in V1.22 Current FW V1.23 Error in V1.0: The structure element dpc31.com_user_ram_segments is not initialized in the existing example. Work around: Initialisation in sys_main.c with DPC31_USER_RAM_SEGMENTS Error in V1.0: Please check that the Temp-Buffer is fixed to 256 instead of 32. Error in V1.2 Please correct function call in
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		V1SL_OK != pbc_open(&,&handle)
V1SL	16 bit values of the inputs and acyclic data are exchanged when using an Intel-Mode Controller	Please exchange the 16 Bit values when using the example program in the DP/PA-Kit in combination with the CP5613-program
V1SL	The use of variables inside the memory of the DPC31 causes wrong reactions	In the structure V1SL_STRUC_PBC_DETAIL the variable com_user_ram_segments has to be set to DPC31_USER_RAM_SEGMENTS - x. x = number of segments which are needed to store the variables. Afterwards store data at the correct address.
V1SL	The use of ISOCHRON-mode as a DPV2-slave causes a wrong reaction	Error in FW V2.00: ISOCHRONBIT must be allowed
V1SL	The C2- communicationchannels don't checks the lengths in the INITIATE-telegrams	Error in FW V2.10: Checking of the lengths is necessary
V1SL	After a reconnection of the slave the first inputdata have old value	Error in FW V2.20: Don't take the first inputdata after reconnect
V1SL		Current FW V2.30